Educational ECAD User Group

Autumn Workshop 2005
7th – 8th September 2005
University of Essex, UK

Call for Contributions
"Teaching Modern Digital Systems: System on a Chip design, fabrication and test"

Digital systems design has undergone a revolution over the last decade with the introduction of hardware description languages (HDLs) and the latest generation field programmable gate arrays (FPGAs). Where once complex digital systems could only be discussed within the classroom environment, the ability to incorporate the cost-effective physical design and prototyping of complex "System in a Chip" (SoC) designs is a realistic option. The advances, if considered carefully, can be used to significantly enhance the teaching and learning of digital systems design from basic processor cores through to high-speed communications networks. The EEUG Autumn Workshop 2005 is to be held on the 7th-8th September 2005 at the University of Essex. Contributions are sought from those involved in (or have planned involvement in) teaching and research activities, in the following (but not exclusive) areas:-

- Developing courseware for SoC design, fabrication and test (SoC or System in a Package (SiP)).
- Hardware Description Languages (HDLs).
- Education activities incorporating programmable logic devices.
- Hardware/software co-design.
- Synthesis.
- Reliability and safety issues.
- Modelling and simulation.
- Computer Aided Design (CAD) and Computer Aided Test (CAT).
- Teaching and learning experiences.
- Activities in National and European based Educational research projects.

EEUG membership is drawn from all areas of Electronics, Electrical Engineering, Computer Science and associated disciplines. EEUG workshops bring together a wide range of practitioners to exchange news, views, experiences and opinions, and attracts an audience from a variety of Higher Education institutions, together with delegates from RAL and industry. Information about previous workshops is available at the EEUG Website (http://www.eeug.org.uk), or from the EEUG Chairman (details below).

Three types of contribution are sought:

- Oral presentations, which are informal in nature, and should be 20 minutes in duration.
- Demonstrations of research and teaching material.
- Poster presentations detailing current research projects. In particular, we would encourage the presentation of postgraduate student research project work.

Offers of contribution (title and brief synopsis (maximum 1 page)) should be sent by 30th June 2005 (with full papers by 31st August 2005), to:

Dr I A Grout, (Chair, EEUG)
Department of Electronic and Computer Engineering,
University of Limerick (Ollscoil Luimnigh), Limerick, Ireland

Tel : (+353) 61 202298
Fax : (+353) 61 338176
Email : Ian.Grout@ul.ie

Further details can found on the EEUG website (http://www.eeug.org.uk) or directly from the EEUG Chair.